

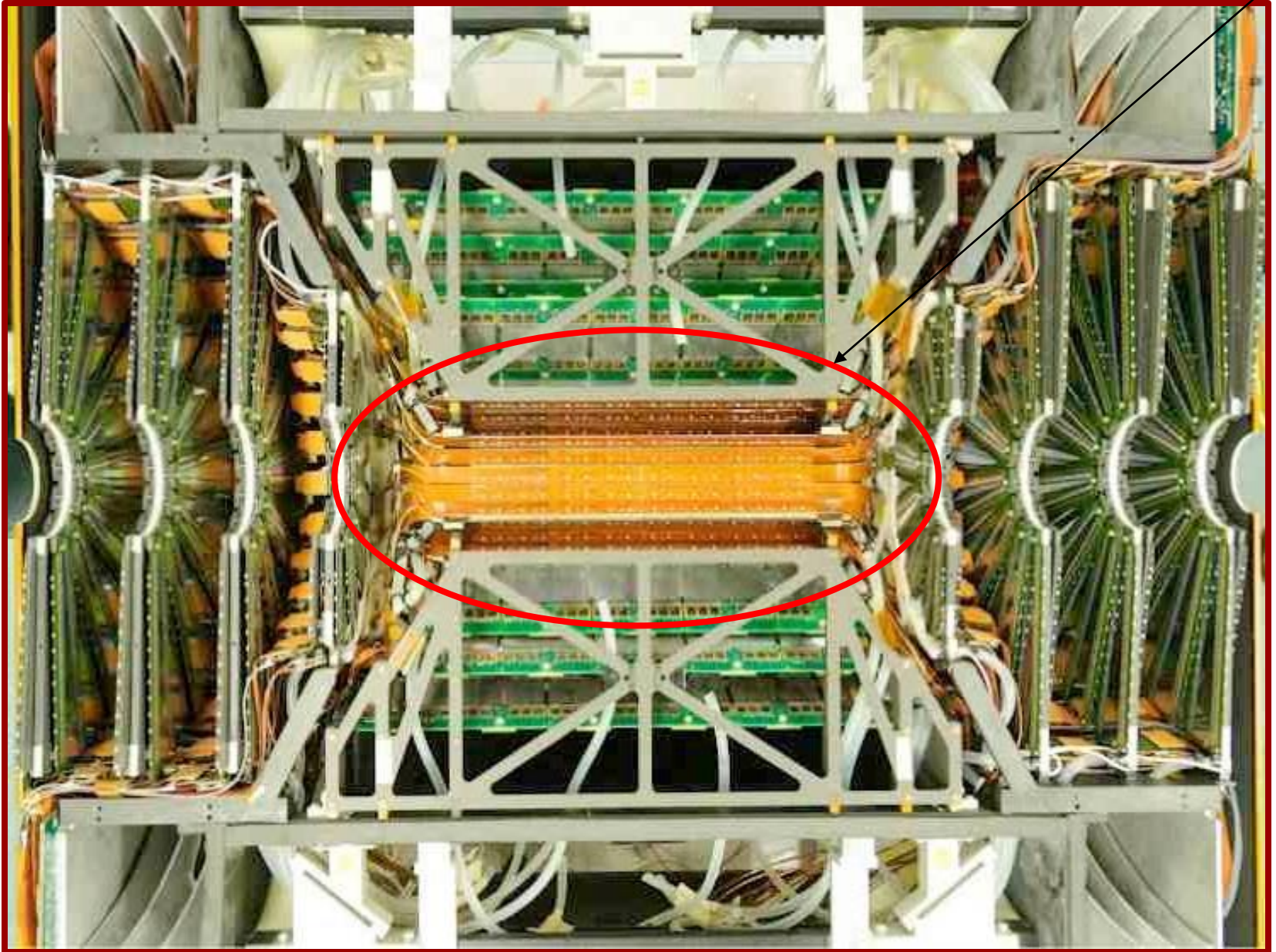
# Integration of the Silicon Pixels into sPHENIX

Rachid Nouicer

Richard Ruggiero and Chris Pontieri

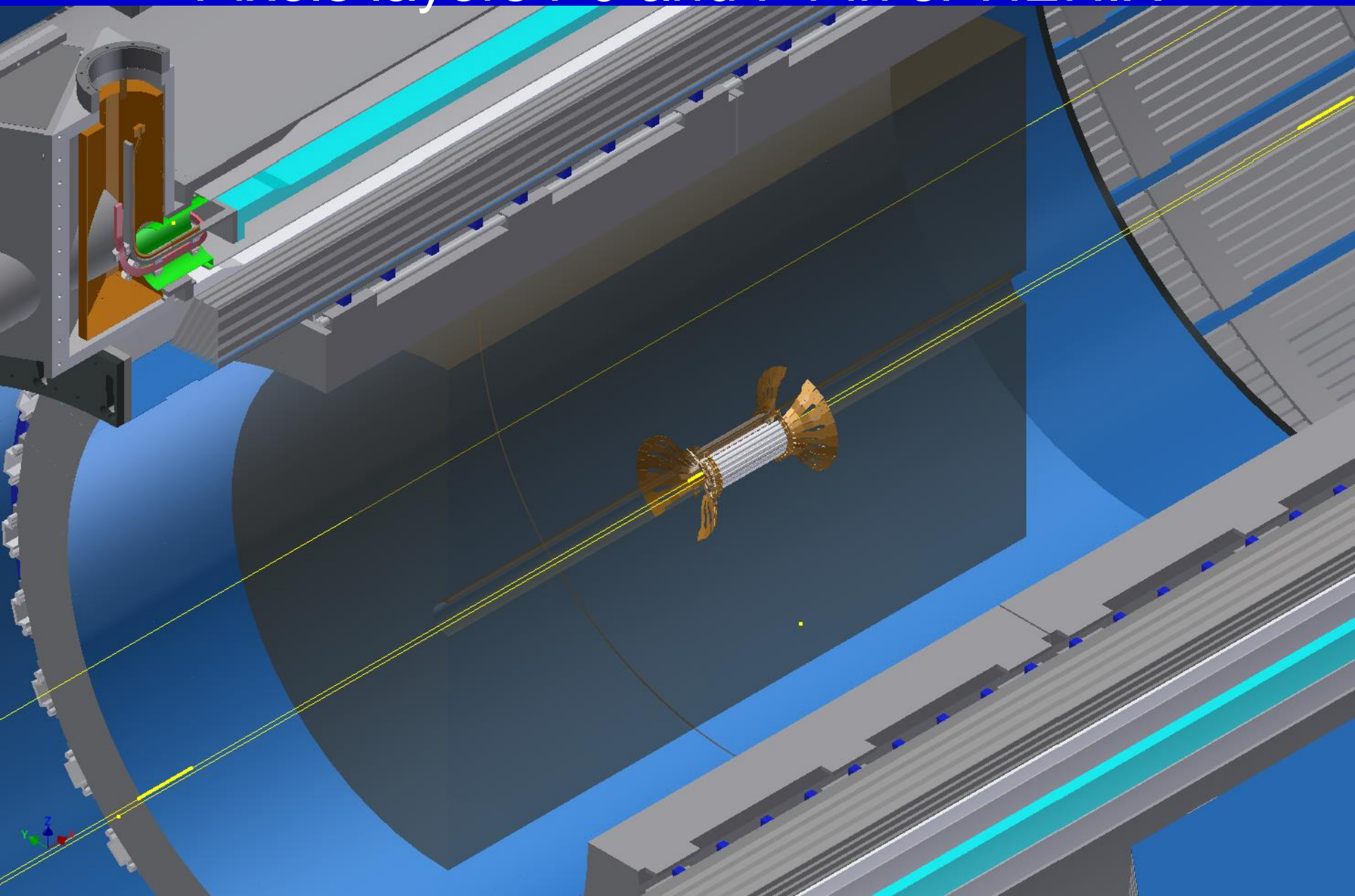
Brookhaven National Laboratory (BNL)

# How to integrate Silicon Pixels into sPHENIX?

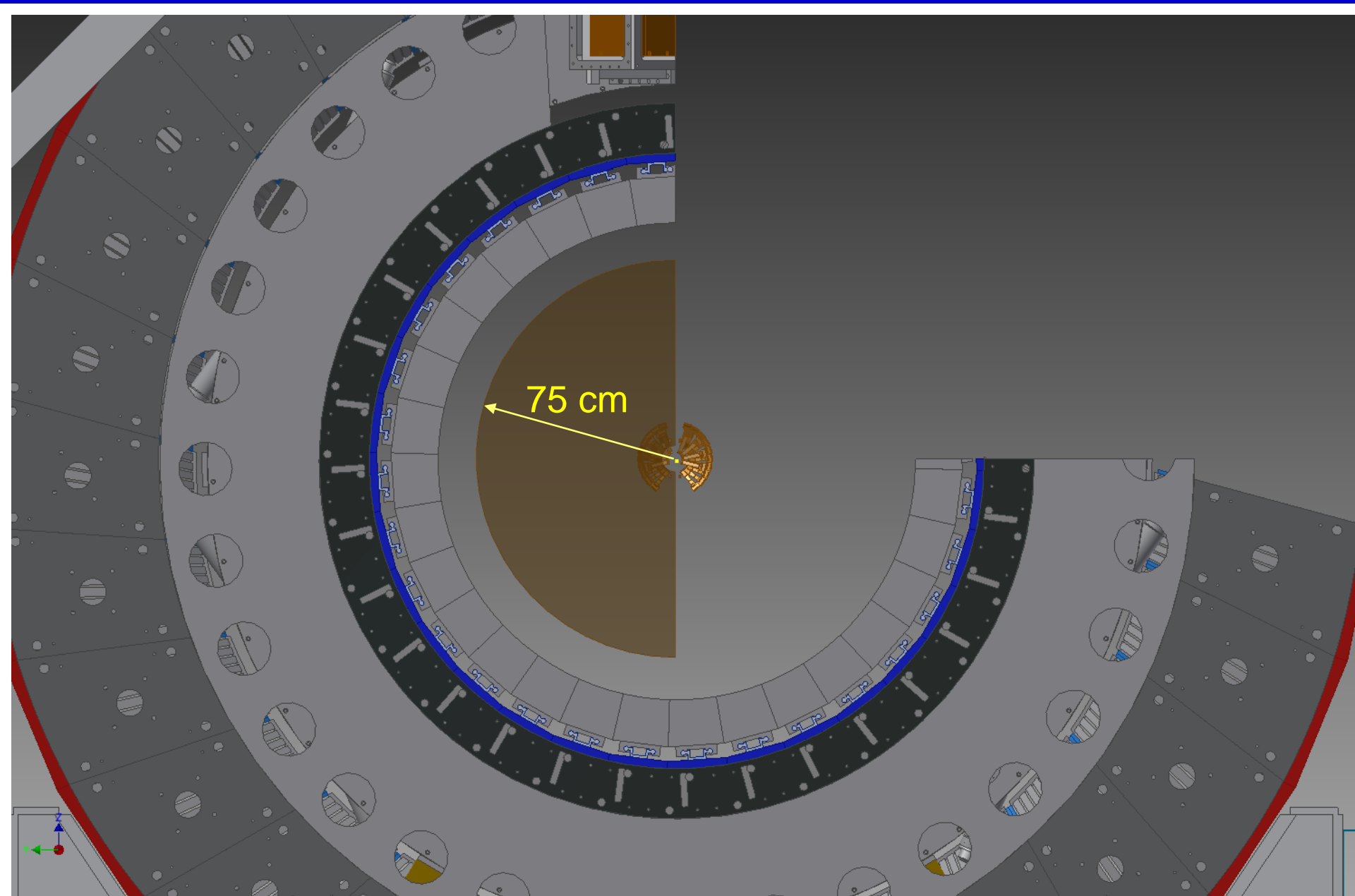




# Pixels layers P0 and P1 in sPHENIX



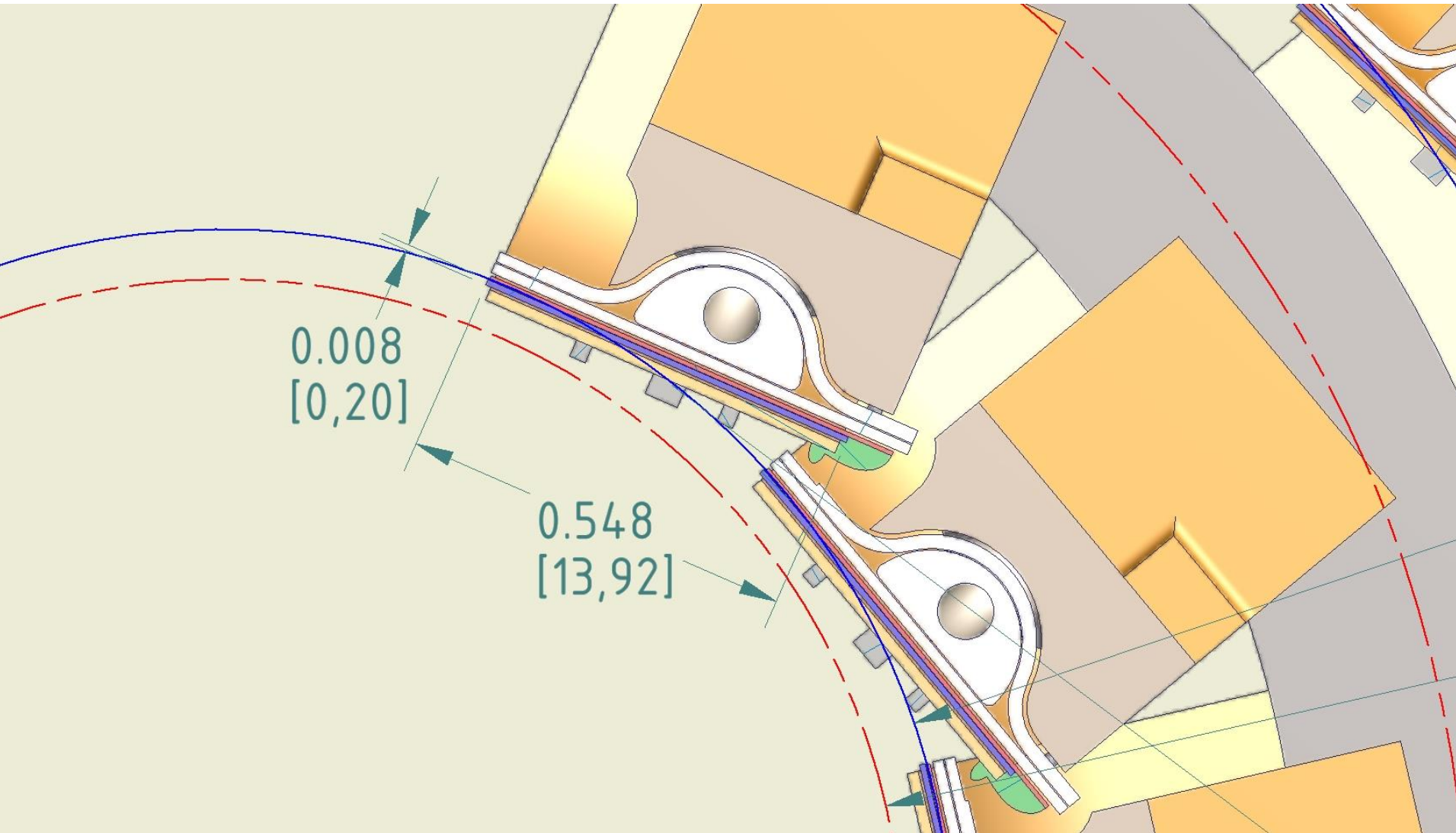
# Tracking Envelop in sPHENIX: 75 cm



# To do List

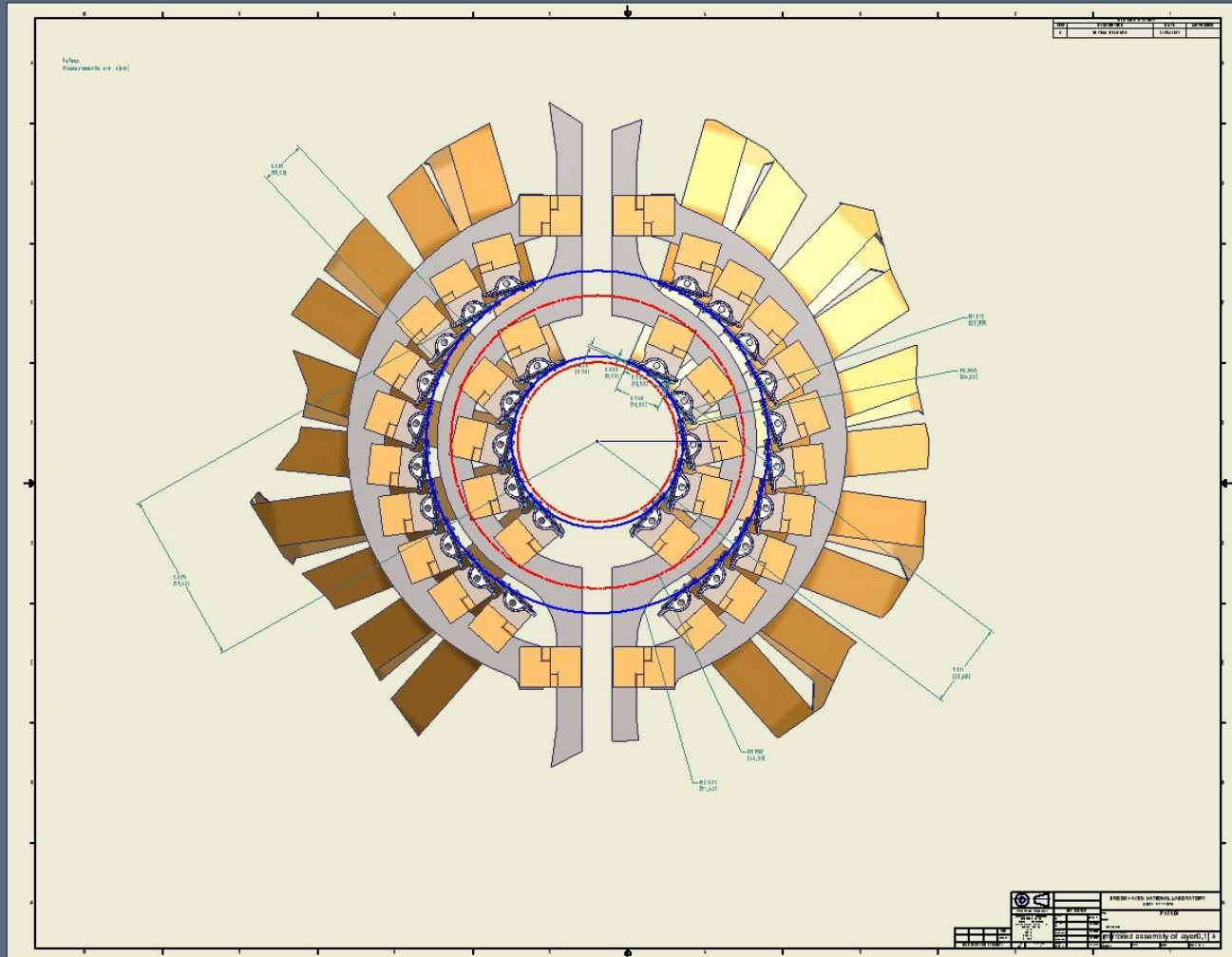
- How many ladders are needed for P0 and P1 to have full coverage in sPHENIX?

Knowing:  $r_0$  (P0) = 2.48 cm and  $r_1$  (P1) = 3.63 cm.



# To do List

- How many ladders are needed for P0 and P1 to have full coverage in sPHENIX?  
Knowing:  $r_0$  (P0) = 2.48 cm and  $r_1$  (P1) = 3.63 cm.





# To do List

- How many ladders are needed for P0 and P1 to have full coverage in sPHENIX?

Knowing:  $r_0$  (P0) = 2.48 cm and  $r_1$  (P1) = 3.63 cm.

Preliminary Answer:

at:  $r_0$  (P0) ~ 2.4 cm, we need 12 ladders of Pixels for full coverage.

